Assessment: Course Four Column

Spring/Summer 2017

Inactive Date: Comments:



El Camino: Course SLOs (IND) - Electronics and Computer Hardware Technology

ECC: ECHT 122:Semiconductor Circuits II

Course SLOs	Assessment Method Description	Results	Actions
SLO #1 Advanced In-Circuit Measurements - The student will make advanced "in-circuit" measurements using Bench and Portable Digital Multimeter (DMM), Oscilloscope, and Voltage Ohm (VOM), Milliamp Meter on Solid- State-Systems Course SLO Status: Active Course SLO Assessment Cycle: 2016- 17 (Spring 2017), 2018-19 (Spring 2019) Input Date: 11/12/2013 Inactive Date: Comments::	Laboratory Project/Report - Using a lab the requires both Scope and DMM measurements, students must measure and record the data correctly. Standard and Target for Success: 75% of the students must be able to to this correctly without help. Additional Information:	Semester and Year Assessment Conducted: 2016-17 (Spring 2017) Standard Met?: Standard Not Met Of the ten teams of students, 6 out of ten were able to perform the task correctly and without help. The 4 teams that did not do this correctly were able to make the measurements, but had problems in recording the correct units. In these cases they failed to record the parameter being measured and unit measure used with the measurement. For example, Milli-seconds and Microseconds. (04/19/2017) % of Success for this SLO: Faculty Assessment Leader: Robert (Bob) Diaz Faculty Contributing to Assessment:	Action: Begin every advanced class with a one week review of basic electronics. (06/15/2018) Action Category: Teaching Strategies
SLO #2 Field Effect Amplifier - Given a schematic diagram of a basic Field Effect Amplifier, the students will be able to assemble, test and measure the circuit for its operational parameters. Course SLO Status: Active Course SLO Assessment Cycle: 2016-17 (Spring 2017), 2018-19 (Spring 2019) Input Date: 11/12/2013	Laboratory Project/Report - Given a schematic for a basic FET amplifier circuit students must be able to assemble, test, and measure the circuit's parameters. Standard and Target for Success: 75% of the teams should be able to do this without needing help. Additional Information:	Semester and Year Assessment Conducted: 2016-17 (Spring 2017) Standard Met?: Standard Not Met Of the 10 teams only 6 of them were able to do this without needing help. (05/09/2017) % of Success for this SLO: Faculty Assessment Leader: Robert (Bob) Diaz Faculty Contributing to Assessment:	Action: Include a basic electronics review at the start of each advanced class. (06/15/2018) Action Category: Teaching Strategies

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ECC: ECHT 140:Computer Systems and Hardware Technology I

ECHT 140 SLO 2 #Component

Course SLOs	Assessment Method Description	Results	Actions
SLO #1 Course Notebook Students - The students will assemble and maintain a five-section course notebook. Course SLO Status: Active Course SLO Assessment Cycle: 2016- 17 (Spring 2017) Input Date: 11/12/2013 Inactive Date: Comments::	It is expected that 80% of the students will score 18 or above per notebook review evaluation for this SLO. Based on Rubric: 20 items expected to be assessed (3) Students achieved a Full Mastery level, will identify 100-90%, through instructor's review for content and accuracy (2) Students achieved a Partial Mastery level, will identify 89-70%, (17.5-14 items through instructor's review for content and accuracy (3) Students achieved a Non Mastery level, will be able to identify 69-0%, (13.5 -0 items) through instructor's review for content and accuracy	Semester and Year Assessment Conducted: 2016-17 (Spring 2017) Standard Met?: Standard Met N= 13 7/13	Action: Develop a reproducible template that students will use to ensure notebook consistency (05/17/2018) Action Category: Teaching Strategies
	Standard and Target for Success: It is expected that 80% of the students will score 18 or above per notebook review evaluation for this SLO. Additional Information: Related Documents:		

SLO #2 Component Handling

Techniques - The student will be able demonstrate their knowledge of an to demonstrate their knowledge in proper component handling techniques, especially regarding (ESD), Electrostatic Discharge.

Course SLO Status: Active Course SLO Assessment Cycle: 2016-

17 (Spring 2017) **Input Date:** 11/12/2013

Inactive Date: Comments::

Exam/Test/Quiz - The student will ESD/EOS safe work area is to prevent damage to sensitive components from spikes and static discharges. These areas must be designed and maintained to prevent ESD/EOS damage.

Standard and Target for Success:

Based on Percentages: "It is expected that 85% of students will score 75% or above on this SLO.

Based on Rubric: "It is expected that X% of students will score 4 or above on this SLO." Mastery Level 3 (100-85 %) Assignment/Assessment possible)

Students demonstrate their knowledge of an ESD/EOS safe work area is to prevent damage to sensitive components by answering 15-13 questions correctly out of 15

Partial Mastery Level 3 (84-70 %) Assignment/Assessment possible)

Students demonstrate their knowledge of an ESD/EOS safe work area is to prevent damage to sensitive components by answering 12-10 questions correctly out of 15

Non Mastery Level 3 (69-0 %) Assignment/Assessment possible)

Semester and Year Assessment Conducted: 2016-17 (Spring 2017)

Standard Met?: Standard Met

Based on Percentages: "It is expected that 85% of students will score 75% or above on this SLO.

Based on Rubric: "It is expected that X% of students will score 4 or above on this SLO."

Mastery Level 3 (100-85 %) Assignment/Assessment possible)Students demonstrate their knowledge of an ESD/EOS safe work area is to prevent damage to sensitive components by answering 15-13 questions correctly out of 15

Partial Mastery Level 3 (84-70 %) Assignment/Assessment possible)Students demonstrate their knowledge of an ESD/EOS safe work area is to prevent damage to sensitive components by answering 12-10 questions correctly out of 15

Non Mastery Level 3 (69-0%) Assignment/Assessment possible)Students demonstrate their knowledge of an ESD/EOS safe work area is to prevent damage to sensitive components by answering 9-0 questions correctly out of 15

(06/04/2017)

% of Success for this SLO:

Faculty Assessment Leader: Steve Cocca **Faculty Contributing to Assessment:**

Action: The college needs to purchase and install ESD equipment and furniture to be used in all ECHT, ESD sensitive (05/14/2020)areas

Action Category:

Program/College Support

Additional Information:

Course SLOs Assessment Method Description

Results

Actions

SLO #3 Computer Estimate and Configuration - The student will be able to demonstrate their ability to cost out and configure either a Business or "Gaming" Computer per customer specifications.

Course SLO Status: Active Course SLO Assessment Cycle: 2016-

17 (Spring 2017)

Input Date: 11/12/2013

Inactive Date: Comments::

Laboratory Project/Report -

Students, will research various computer components and configurations to build either a gaming or business computer system a per specification with a \$5,000 US dollar budget.

Standard and Target for Success:

It is expected that 80% of the students will score 2 or above this SLO.

Based on Rubric: 20 items expected to be assessed
(3) Students achieved a Full Mastery level, will identify 100-90%, (20-18 items) various computer components to use in building a specific pc type as required, within budget

- (2) Students achieved a Partial Mastery level, will identify 89-70%,
 (17.5-14 items) various computer components to use in building a specific pc type as required, within budget
- (3) Students achieved a Non Mastery level, will be able to identify 69-0%, (13.5 -0 items) various computer components to use in building a specific pc type as required, within budget

Semester and Year Assessment Conducted: 2016-17 (Spring 2017)

Standard Met?: Standard Not Met N= 13

- 3/20 3 Students achieved a Full Mastery level, 100-90%, will identify various computer parts and build a specific pc type as required, within budget.
- 5/1 2 Students achieved a Partial Mastery level, 89-70%, will identify various computer parts and build a specific pc type as required, within budget
- 6/5 1 Students achieved a Non Mastery level, 69-0, will identify various computer parts and build and build a specific pc type as required, within budget

(06/04/2017)

% of Success for this SLO:

Faculty Assessment Leader: Steve Cocca Faculty Contributing to Assessment: Action: Have students present and defend their system specifications in front of their peers Spring 2018 Teaching Strategies

(05/14/2018)

Action Category: Teaching Strategies

Additional Information:

ECC: ECHT 144:CompTIA A+ Certification Preparation for Computer Hardware Systems

Course SLOs	Assessment Method Description	Results	Actions
SLO #1 Course Notebook - The students will assemble and maintain a five-section course notebook. Course SLO Status: Active Course SLO Assessment Cycle: 2016-17 (Spring 2017) Input Date: 11/12/2013 Inactive Date: Comments::	Multiple Assessments - Students, will prepare and submit for evaluation a "5" section notebook to be used for both quizzes and test. The students' notebook evaluation will be based on content and accuracy	Semester and Year Assessment Conducted: 2016-17 (Spring 2017) Standard Met?: Standard Met N= 17 13/20 3 Students achieved a Full Mastery level, 100-90%,) content and accuracy	Action: Develop a reproducible template that students will use to ensure notebook consistency (05/14/2018) Action Category: Teaching Strategies
	Standard and Target for Success: It is expected that 80% of the students will score 18 or above per notebook review evaluation Additional Information:	 3/1 2 Students achieved a Partial Mastery level, 89-70%, content and accuracy 1/5 1 Students achieved a Non Mastery level, 69-0, content and accuracy (06/04/2017) % of Success for this SLO: Faculty Assessment Leader: Steve Cocca Faculty Contributing to Assessment: 	
SLO #2 CompTIA Industry Certification - The student will acquire a knowledge base to prepare to take the A+ Certification Exam through CompTIA, an industry recognized certification. Course SLO Status: Active Course SLO Assessment Cycle: 2016- 17 (Spring 2017) Input Date: 11/12/2013 Inactive Date: Comments::	Laboratory Project/Report - Students, will be given desktop PC where an examination of their knowledge will be assessed in the identification of systems components and hardware, and describe proper installation techniques required to build and maintain a desktop PC Standard and Target for Success: It is expected that 80% of students will score 2 or above on this SLO. Additional Information:	Semester and Year Assessment Conducted: 2016-17 (Spring 2017) Standard Met?: Standard Met N= 13 11/13 3 Students achieved a Full Mastery level, 100- 90%, will identify to component level and describe proper installation techniques required to build and maintain a desktop PC 1/13 2 Students achieved a Partial Mastery level, 89- 70%, will identify to component level and describe proper installation techniques required to build and maintain a desktop PC	Action: Provide students with more outside lab time to help reinforce areas of student deficiencies. Spring 2018 Progrsm/College (05/14/2018) Action Category: Program/College Support

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1/13 1 Students achieved a Non Mastery level, 69-0%,

Course SLOs	Assessment Method Description	Results	Actions
		will identify to component level and describe proper installation techniques required to build and maintain a desktop PC (06/04/2017) % of Success for this SLO: Faculty Assessment Leader: Steve Cocca	
		Faculty Contributing to Assessment:	
SLO #3 Electricity & Electronics - The student will acquire a knowledge in safety and the basics of electricity and electronics, micro-computer hardware and components. Course SLO Status: Active Course SLO Assessment Cycle: 2016-17 (Spring 2017) Input Date: 11/12/2013 Inactive Date: Comments::	Exam/Test/Quiz - The student will demonstrate and apply their basic knowledge of electronics as it pertains to a computer service technical environment Standard and Target for Success: Standard and Target for Success: 85% (21 out of 25 items correct) Additional Information:	Semester and Year Assessment Conducted: 2016-17 (Spring 2017) Standard Met?: Standard Met N= 17 13/17 Mastery Level 3 (100-85 %) Assignment/Assessment possible)Students demonstrate a "Mastery" of their knowledge and apply their basic knowledge of electronics as it pertains to a computer service technical environment by answering 25-21 questions correctly out of 25	Action: Recommend students take ECHT 11 Prior to taking ECHT 144 (05/14/2020) Action Category: Program/College Support
		2/17 Partial Mastery Level 3 (84-70 %) Assignment/Assessment possible)Students "Partially Master" demonstrate their knowledge and apply their basic knowledge of electronics as it pertains to a computer service technical environment by answering 20-18 questions correctly out of 25	
		2/17 Non Mastery Level 3 (69-0 %) Assignment/Assessment possible S)tudents demonstrate a "Non Mastery" in their knowledge and apply their basic	

correctly out of 25

knowledge of electronics as it pertains to a computer service technical environment by answering 17-0 questions

Course SLOs	Assessment Method Description	Results	Actions

(06/04/2017)

% of Success for this SLO:

Faculty Assessment Leader: Steve Cocca **Faculty Contributing to Assessment:**

Related Documents:

ECHT 144 SLO 3 #Component Handling Techniques.doc

ECC: ECHT 191:Introduction to Microprocessors and Interfacing

100% of the students must meet this

Even with help, students quit or are unable to do the task, level 1. No student should meet this level.

level.

Course SLOs	Assessment Method Description	Results	Actions
SLO #1 Machine Assembly Language - Students will demonstrate their knowledge of fundamentals of machine assembly language Course SLO Status: Active Course SLO Assessment Cycle: 2014- 15 (Summer 2015), 2016-17 (Spring 2017) Input Date: 11/12/2013 Inactive Date: Comments::	Laboratory Project/Report - Given a schematic and description of the problem, students will build the circuit and write code to preform the function. Standard and Target for Success: The hardware and software must work as specified: Students complete the task correctly without any help, level 4, 70% of the students meet this standard. Additional Information:	Semester and Year Assessment Conducted: 2016-17 (Spring 2017) Standard Met?: Standard Not Met Of the 12 teams doing this assignment, 6 were able to do this without any help and 6 needed additional help. (05/09/2017) % of Success for this SLO: Faculty Assessment Leader: Ro Faculty Contributing to Assessment:	Action: Include a summery of instructions required to do the task and a summery of how to program the board. (06/15/2018) Action Category: Teaching Strategies
SLO #2 Digital & Analog Interfacing - Students will demonstrate their use of software to simulate hardware and digital and analog interfacing. Course SLO Status: Active Course SLO Assessment Cycle: 2013- 14 (Spring 2014), 2014-15 (Summer 2015), 2016-17 (Spring 2017) Input Date: 11/12/2013 Inactive Date: Comments::	Laboratory Project/Report - Given a software and hardware problem, the students must correctly build the hardware and write the code to make the hardware work correctly. Standard and Target for Success: The hardware and software must work as specified: Students are able to complete the task correctly without any help, level 4. 70% of the students must meet this level. Students need a minor amount of help or hints to complete the task, level 3. 80% of the students must meet this level or a higher level. Students need a major amount of help to complete the task, level 2.	Semester and Year Assessment Conducted: 2016-17 (Spring 2017) Standard Met?: Standard Met Of the 12 teams 9 were able to do this without requiring any additional help. Three teams required minor help. (04/11/2017) % of Success for this SLO: Faculty Assessment Leader: Robert (Bob) Diaz Faculty Contributing to Assessment:	Action: Add summery of instructions needed for lab and a summery of how to use the software interface. (06/29/2018) Action Category: Teaching Strategies

Results

Actions

Additional Information: Because there's more than one way of writing correct code, any solution that correctly does the task is considered successful.

SLO #3 Microprocessors and

Course SLOs

Microcontrollers - Students will
demonstrate their knowledge of
microprocessors and microcontrollers
as they relate to industrial and
consumer equipment.multiple choic
answer the qu
their abilities.Standard and
Students are a

Course SLO Status: Active Course SLO Assessment Cycle: 2014-15 (Summer 2015), 2016-17 (Spring 2017)

Input Date: 11/12/2013

Inactive Date: Comments::

Exam/Test/Quiz - Given a written multiple choice test, students will answer the questions to the best of their abilities.

Standard and Target for Success:

Students are able to pass the test with an 90% or higher; level 4, mastery level. Minimum of 60% of the class should meet this level. Students are able to pass the test with an 80% or higher; level 3, strong understanding. Minimum of 70% of the class should meet this level or get a higher level. Students are able to pass the test with an 70% or higher; level 2, basic understanding. Minimum of 80% of the class should meet this level or get a higher level.

Students are unable to pass with a 70% or higher; level 1, fell shot of understanding. Maximum of 20% of the class should be at this level.

Additional Information:

Semester and Year Assessment Conducted: 2016-17 (Spring 2017)

Standard Met?: Standard Not Met

Of the 22 students in the class, 9 passed (41%) Test #1 with 90% or higher, 18 passed (82%) with a score of 80% or higher, 20 passed (91%) with a score of 70% or higher. Thus the A students were not as high as I hopped for, but the B and C students did meet my expectations. (06/08/2017)

% of Success for this SLO:

Faculty Assessment Leader: Robert (Bob) Diaz

Faculty Contributing to Assessment:

Action: Expand and rewrite the study notes for the class.

(07/02/2018)

Action Category: Teaching

Strategies